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FIG. 1 (a)

|         |         |         |         |     |          |          |
|---------|---------|---------|---------|-----|----------|----------|
| AS<br>0 | AS<br>1 | DS<br>0 | DS<br>1 | ... | DS<br>43 | DS<br>44 |
|---------|---------|---------|---------|-----|----------|----------|

FIG. 1 (b)

|             |                    |                  |         |
|-------------|--------------------|------------------|---------|
| CM<br>field | Pre-Amble<br>field | Address<br>field | Reserve |
|-------------|--------------------|------------------|---------|

FIG. 1 (c)

|             |                    |               |                     |
|-------------|--------------------|---------------|---------------------|
| CM<br>field | Pre-Write<br>field | Data<br>field | Post-Write<br>field |
|-------------|--------------------|---------------|---------------------|



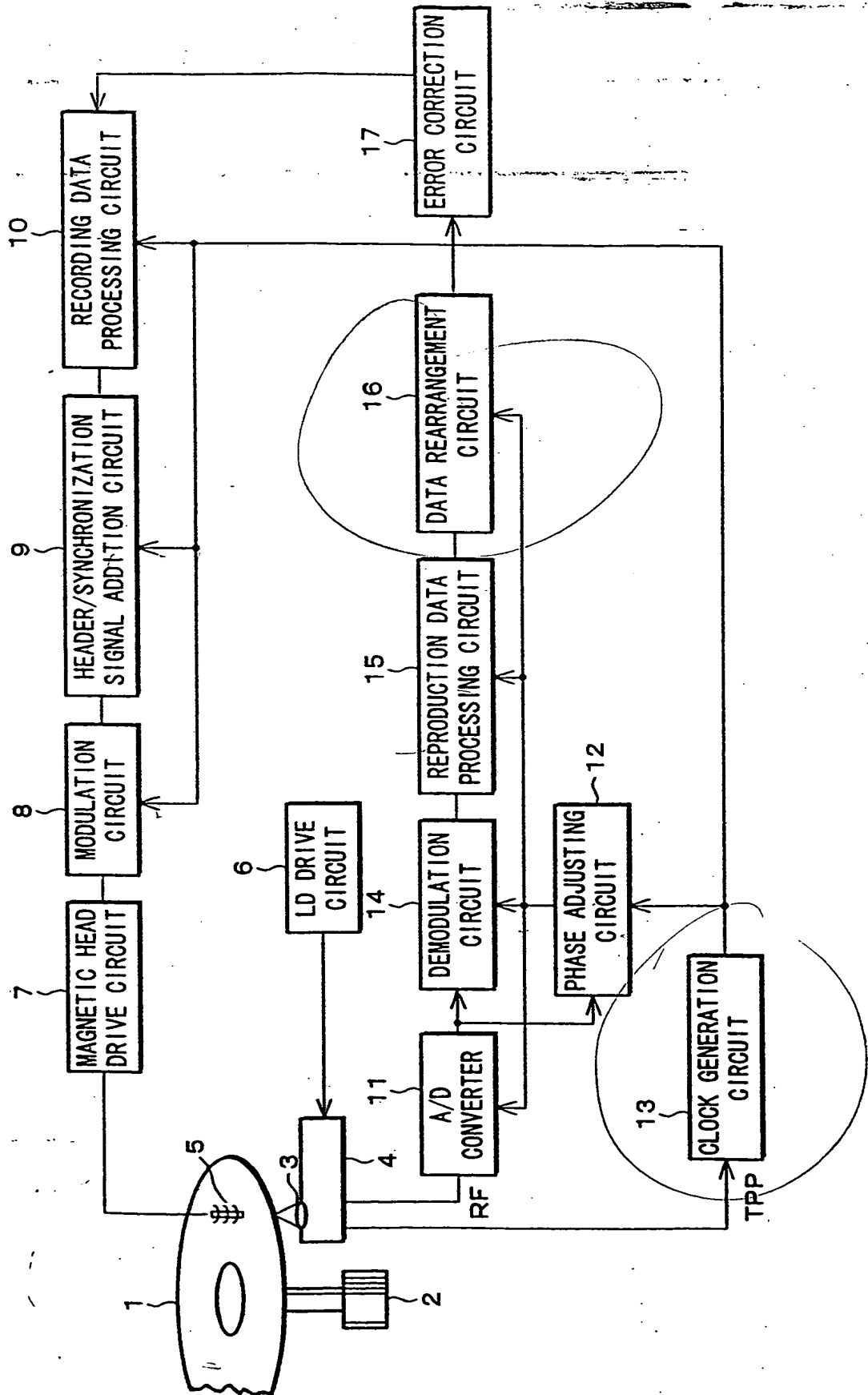
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FIG. 2

| SEGMENT<br>NUMBER<br>RECORDING<br>DATA | DS0    | DS1 |      | DS2  | DS3 |      | ... |  | DS43 | DS44 |
|--|--------|-----|------|------|-----|------|-----|--|------|------|
|  | Header | S   | Data | Data | S   | Data | ... |  | S    | Data |
|  |        |     |      |      |     |      |     |  |      |      |

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FIG. 3



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FIG. 4

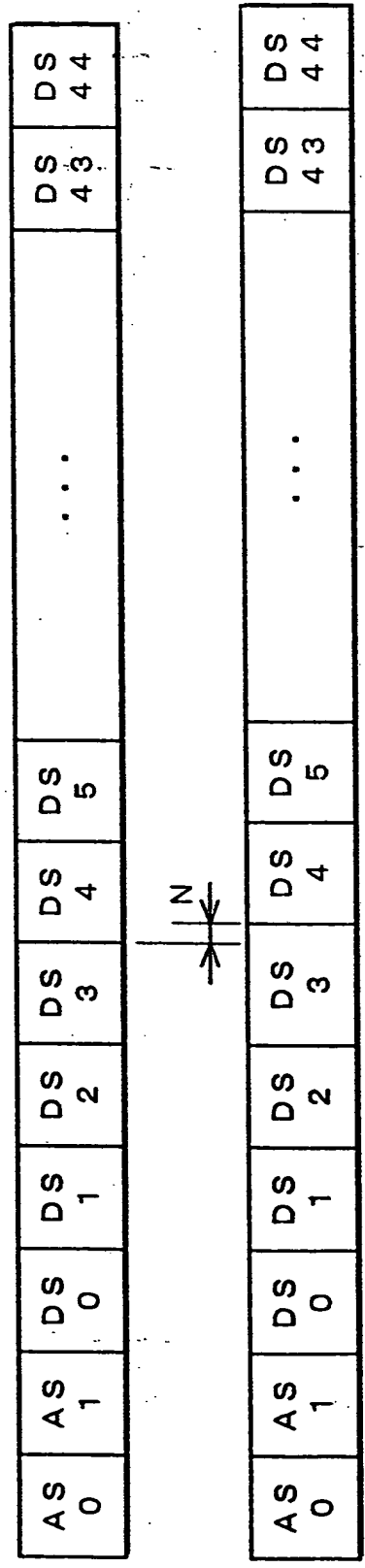
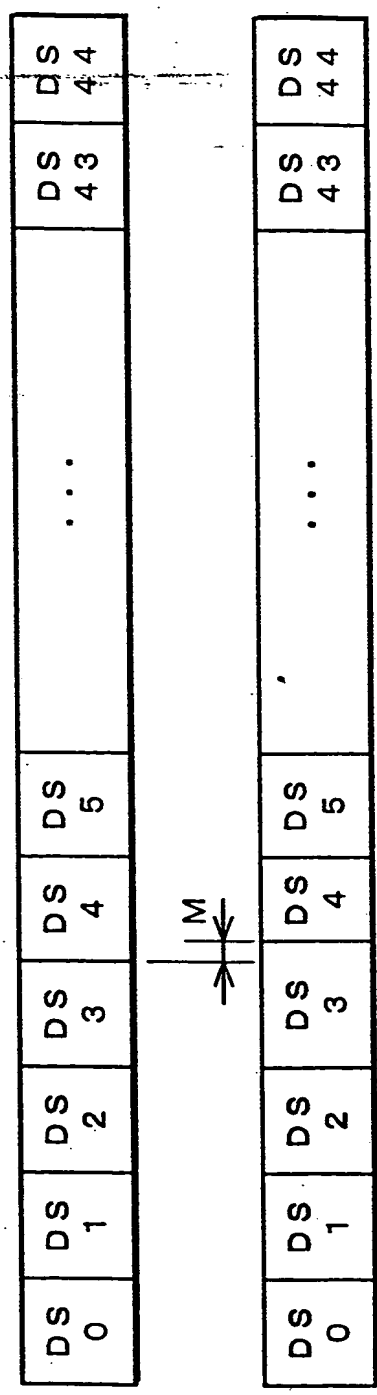


FIG. 5





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FIG. 6 (a) PRIOR ART

|         |         |         |     |          |          |
|---------|---------|---------|-----|----------|----------|
| AS<br>0 | DS<br>0 | DS<br>1 | ... | DS<br>43 | DS<br>44 |
|---------|---------|---------|-----|----------|----------|

FIG. 6 (b) PRIOR ART

|             |                    |                  |         |
|-------------|--------------------|------------------|---------|
| CM<br>field | Pre-Amble<br>field | Address<br>field | Reserve |
|-------------|--------------------|------------------|---------|

FIG. 6 (c) PRIOR ART

|             |               |
|-------------|---------------|
| CM<br>field | Data<br>field |
|-------------|---------------|

Timing diagram for a 4-level NRZ signal. The diagram shows a sequence of LAND and GROOVE levels. A central section is labeled "CLOCK MARKS" with a downward arrow. Below the signal levels, a waveform labeled "OV" (overvoltage) shows a peak at the first clock mark and a trough at the second clock mark. Vertical dashed lines align the clock marks with the signal transitions.

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FIG. 8 PRIOR ART

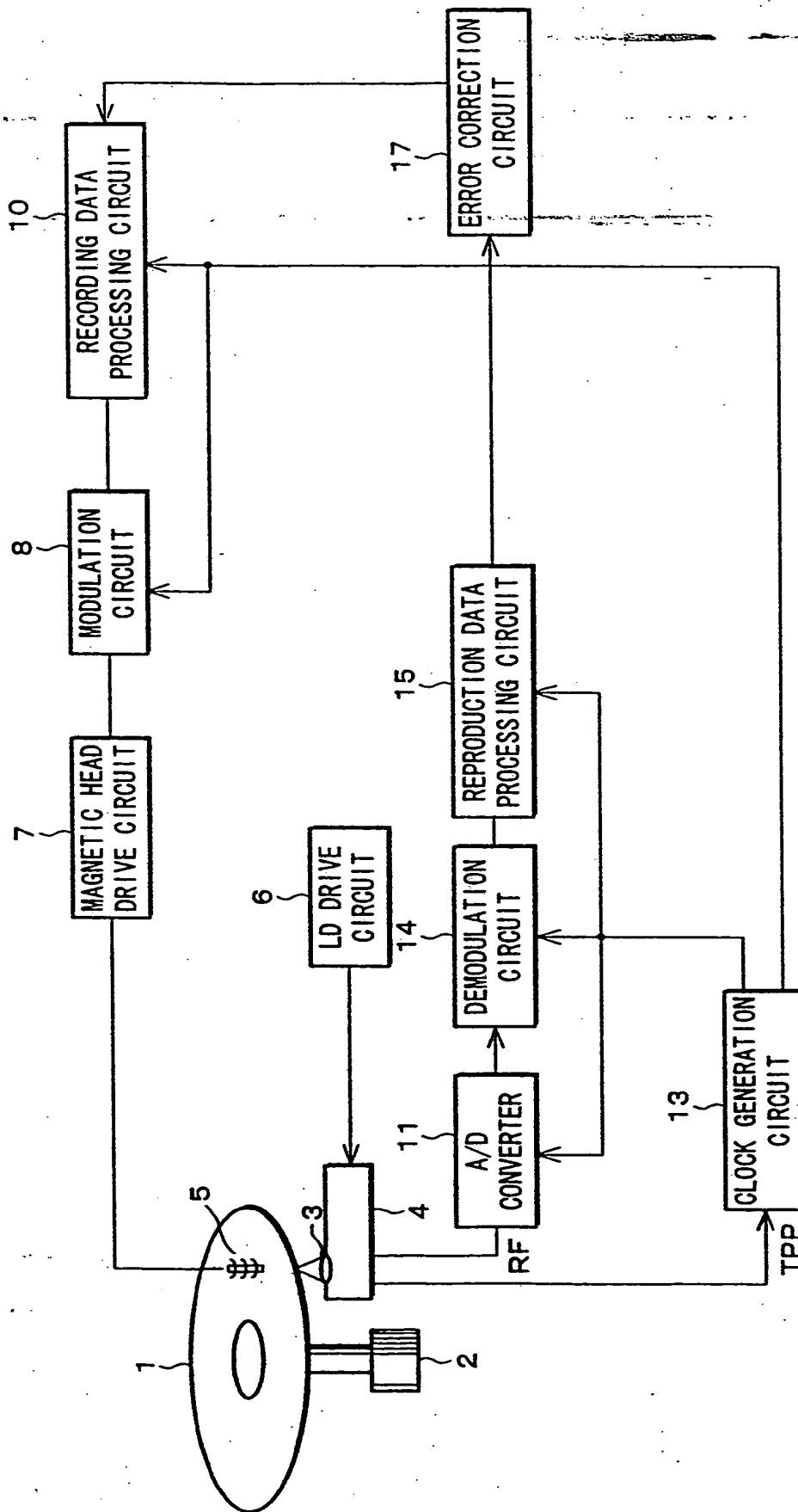




FIG. 9 PRIOR ART

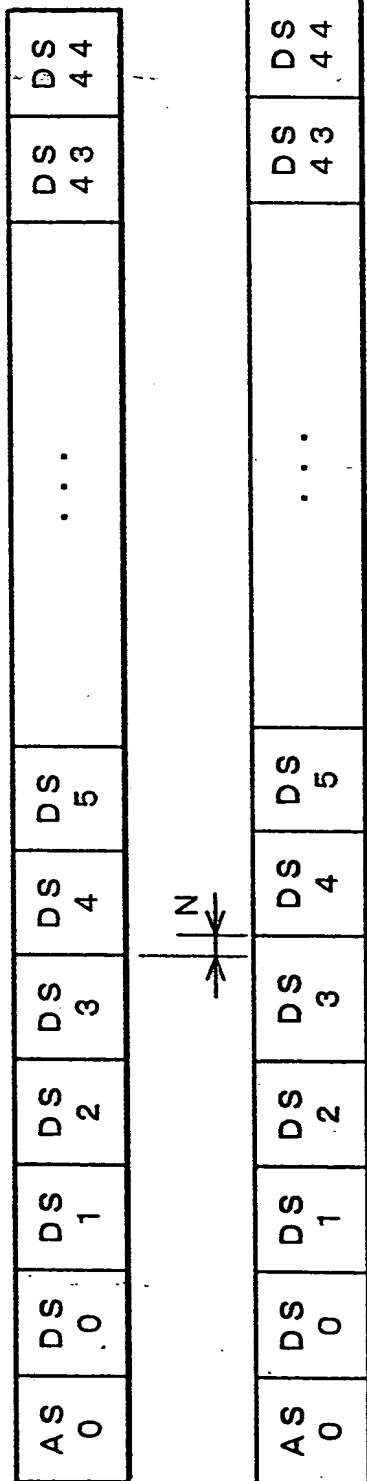
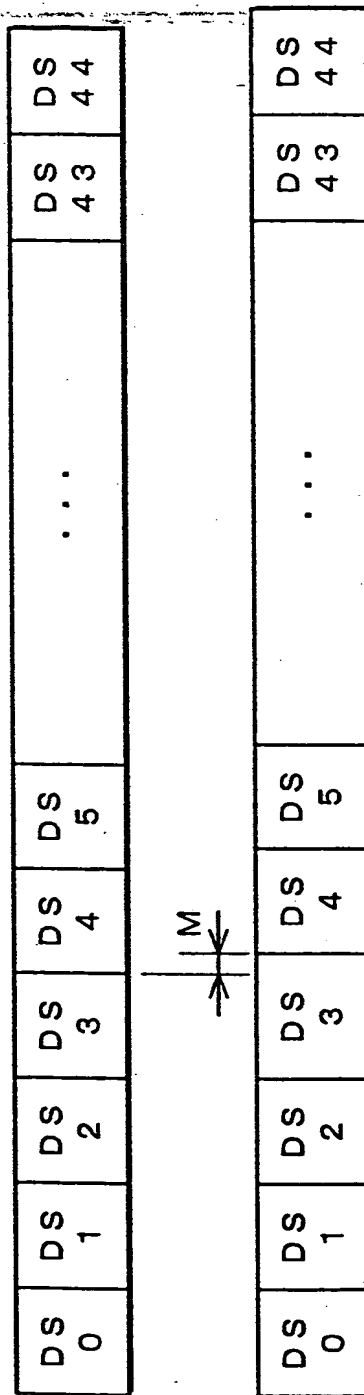


FIG. 10 PRIOR ART



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